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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/340,776 06/28/99 PANESAR

G S1022/8250

EXAMINER

TM02/1002

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CHIAN, T  
ART UNIT PAPER NUMBER

2123  
DATE MAILED:

10/02/01

**Please find below and/or attached an Office communication concerning this application or proceeding.**

**Commissioner of Patents and Trademarks**

# Office Action Summary

Application No.  
09/340,776

Applicant(s)  
Gajinder Singh Panesar

Examiner  
Thai Phan

Art Unit  
2123



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_\_
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claims \_\_\_\_\_ are subject to restriction and/or election requirements.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

- 13) ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☒ All b) ☐ Some\* c) ☐ None of:
- ☒ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \*See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

## Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 16) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s). 05 20) ☐ Other: \_\_\_\_\_

### DETAILED ACTION

This Office Action is responsive to patent application S/N: 09/340,776. Claims 1-7 are pending in this official action.

#### *Priority*

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file. However, the printed application number in the submitted paper does not match with the number as in the Declaration. Correction is required

#### *Drawings*

2. The formal drawing, filed on Aug. 16, 1999 have been received and entered.

#### *Claim Rejections - 35 USC § 102*

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claims 1-7 are rejected under 35 U.S.C. 102(a) as being anticipated by Bona et al., Patent no. 5,710,934.

As per claim 1, Bona anticipated method and system for simulating ASIC design including application specific processor (ASP) identical to the claimed invention (Abstract). According to

Bona, the simulation method includes steps of defining functional model for the processor and peripheral (col. 4, line 1-53), generating interface function file for each peripheral which defines the communication of the peripheral with the processor, simulating in the high level language as part of the functional model an application executable by the CPU and operation of the set of peripheral devices, including for a predetermined simulation phase, test function files, and operation of the set of peripheral devices, outputting the state of the application and the state of the peripherals to the modeling file for converting the modeling file in the high level programming language to a language executable by the simulation system (col. 4, cols. 6-13, "Test File Form"), and simulating the application specific integrated circuit which would include specific processor such as DSP processor, graphic processor, etc. inherently known in the ASIC design art (col. 7, Line 33 to col. 9, line 40).

As per claim 2, Bona anticipated environment stimuli for peripherals.

As per claim 3, Bona anticipated programming language being used to program simulation program which would include C as claimed.

As per claim 4, Bona anticipated description language for functional simulation such as HDL as known for those skilled in the art.

As per claim 5, Bona anticipated a plurality of processors or microprocessor in broad sense to carry out ASIC design simulation.

As per claim 6, claim 6 is directed to system for performing steps of method claim 1 above, and Bona also anticipated system including means as claimed for performing such functional steps as in claim 1; therefore, claim 6 is also rejected in like manner.

As per claim 7, Bona anticipated computer program including modeling file as claimed for performing steps as in method claims above; therefore, claim 7 is also rejected in like manner.

***Conclusion***

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

1. Patent no. 5,684,980 issued to Casselman, Steven, Nov. 1997
2. Patent no. 6,009,256, issued to Tseng et al., Dec. 1999
3. Patent no. 6,016,554, issued to Skrovan et al., Jan. 2000
4. Patent no. 6,058,253, issued to Lowe, William, May 2000

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thai Phan whose telephone number is (703) 305-3812.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703)305-3900.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

(703) 308-9051, (for formal communications intended for entry)

**Or:**

(703) 308-1396 (for informal or draft communications, please label "PROPOSED"  
or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,  
Arlington, VA., Sixth Floor (Receptionist).

September 24, 2001



KEVIN J. TESKA  
SUPERVISORY  
PATENT EXAMINER